# Investigating the Temperature Effects on ZnO, TiO<sub>2</sub>, WO<sub>3</sub> and HfO<sub>2</sub> Based Resistive Random Access Memory (RRAM) Devices

T.D. Dongale<sup>1,\*</sup>, K.V. Khot<sup>2</sup>, S.V. Mohite<sup>3</sup>, S.S. Khandagale<sup>4</sup>, S.S. Shinde<sup>3</sup>, V.L. Patil<sup>3</sup>, S.A. Vanalkar<sup>3</sup>, A.V. Moholkar<sup>3</sup>, K.Y. Rajpure<sup>3</sup>, P.N. Bhosale<sup>2</sup>, P.S. Patil<sup>3</sup>, P.K. Gaikwad<sup>4</sup>, R.K. Kamat<sup>4</sup>

- <sup>1</sup> Computational Electronics and Nanoscience Research Laboratory, School of Nanoscience and Biotechnology, Shivaji University, Kolhapur- 416004, India
  - <sup>2</sup> Department of Chemistry, Shivaji University, Kolhapur 416004, India
    - <sup>3</sup> Department of Physics, Shivaji University, Kolhapur 416004, India
  - <sup>4</sup> Department of Electronics, Shivaji University, Kolhapur, 416004, India

(Received 18 June 2016; published online 29 November 2016)

In this paper, we report the effect of filament radius and filament resistivity on the ZnO,  $TiO_2$ ,  $WO_3$  and  $HfO_2$  based Resistive Random Access Memory (RRAM) devices. We resort to the thermal reaction model of RRAM for the present analysis. The results substantiate decrease in saturated temperature with increase in the radius and resistivity of filament for the investigated RRAM devices. Moreover, a sudden change in the saturated temperature at a lower value of filament radius and resistivity is observed as against the steady change at the medium and higher value of the filament radius and resistivity. Results confirm the dependence of saturated temperature on the filament size and resistivity in RRAM.

Keywords: RRAM, Thermal reaction model, Reliability, Nanoelectronics.

DOI: 10.21272/jnep.8(4(1)).04030 PACS numbers: 73.22. - f; 07.05.Tp.

#### 1. INTRODUCTION

The Resistive Random Access Memory (RRAM) is considered to be a strong candidate which is poised to substitute the conventional flash memories [1]. The RRAM have several advantages over its other counterparts, for instance, high density of data storage, long data retention, low operating voltage, high endurance, fast switching speed and compatibility with conventional CMOS process and so on [1-2]. Generally, physical and chemical mechanism of RRAM is classified in terms of Valency Change Mechanism (VCM) [3-5], Electro-Chemical Metallization (ECM) [6], Thermo-Chemical Mechanism (TCM) [7], Phase Change Mechanism (PCM) [8], and Electrostatic/Electronic Mechanism (EEM) [9] etc. In above all mechanism the resistance of the RRAM switches between two resistance states viz. low resistance state (LRS) and high resistance state (HRS) [10-11]. The popular version of RRAM is known as a memristor which is modeled around the valency change mechanism [12-15].

Temperature plays an important role in resistive switching and many studies have been devoted to finding out the underlying physical mechanism and its effect on memory performance. Recently Shang et al reported the heterostructure based thermally stable transparent RRAM. The reported device exhibits forming-free bipolar resistive switching behavior at room temperature with good memory performance [16]. Wang et al reported the thermoelectric Seebeck effect in oxide-based resistive switching memory. They have investigated the intrinsic electronic transport mechanism by measuring thermoelectric Seebeck effects [17]. Yi et al reported the effect of annealing temperature on graphene oxide-based RRAM. Their results are evident that the lower annealing temperature improves the

memory performance of graphene oxide-based RRAM [18]. Fang et al reported low-temperature switching characteristics and conduction mechanism of HfOx based RRAM. The results suggested that at a lower temperature the switching voltage increases [19]. Tsuruoka et al reported the effect of temperature on Cu—Ta<sub>2</sub>O<sub>5</sub>-based atomic switch and investigated the switching mechanism of the developed device. The results revel that SET and RESET voltage decreases as the temperature increases [20]. Sato et al reported the thermal reaction model of the metal oxide-based RRAM. This model calculates the temperature of the conductive filament and its corresponding effect on RRAM performance [21].

The present manuscript reports investigations of temperature effect on ZnO, TiO<sub>2</sub>, WO<sub>3</sub> and HfO<sub>2</sub> based RRAM devices using thermal reaction model reported in the ref. [21]. The rest of the paper is as follows, after a brief introduction in the first section, the second section deals with the overview of thermal reaction model of RRAM and other computational details. The third section deals with the effect of filament radius and resistivity on the saturated temperature of ZnO, TiO<sub>2</sub>, WO<sub>3</sub> and HfO<sub>2</sub> based RRAM devices. At the end conclusion is portraved.

## 2. THERMAL REACTION MODEL OF RRAM

The temperature of the conductive filament plays an important role in the resistive switching. It is observed that the high current density present in the conductive filament and the aftermaths of temperature effects are unavoidable [22]. Hence, the analysis of temperature effect is of foremost important. Fig. 1 represents the cross-sectional view of the thermal reaction model [21].

\_\_\_

<sup>\*</sup> tdd.snst@unishivaji.ac.in

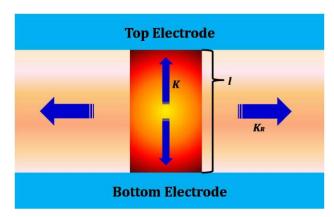


Fig. 1 – Cross sectional view of the model under analysis. This model is popularly known as thermal reaction model of RRAM Device [21]

The model assumes the cylindrical conducting path present in the active layer (oxidation membrane) of the RRAM. The heating temperature  $\Delta T$  can be represented as [21],

$$\Delta T = \frac{1}{\left(K_R + K\right)} \frac{V_{reset}^2}{R} \tag{1}$$

where,  $V_{reset}$  is the RESET voltage of RRAM, R is a resistivity of the filament,  $K_R$  is known as the radial thermal conductance of oxidation membrane and K is the filament thermal conductance and they are given as [21],

$$K_{R} = \frac{2\pi k' l / 4}{\log_{e} (r_{2} / r_{1})}$$
 (2)

$$K = \frac{k\pi r_1^2}{l/2} \tag{3}$$

where k' and k are the thermal conductivities of the materials, l is a thickness of oxidation membrane, and r is the radius of the conductive filament. Table 1 represents the various physical parameters for simulation of different RRAM structures.

Table 1 - Simulation parameters for various RRAM devices

				Density of
materials	conductivity k'(W/cm/°C)		heat <i>c'</i> (J/g/ °C)	material γ'(g/cm)
ZnO	1.2	116	0.5	5.61
${ m TiO}_2$	6.69	21.9	0.6894	4.13
$WO_3$	1.63	173	0.0780	7.16
$HfO_2$	1.1	23	0.144	13.31

In the present analysis thickness of the oxidation membrane is kept constant at 200 nm, and reset voltage at 0.5 V. To analyze the saturated temperature behavior of different RRAM devices, we have varied the filament radius in the range of 10 nm to 100 nm with 10 nm as a step size and filament resistivity in the range of 10  $\mu\Omega$  cm to 100  $\mu\Omega$  cm with 10  $\mu\Omega$  cm as a step size. To analyze the effect of filament radius on different RRAM devices, we have taken three observation of filament resistivity in the order of 10  $\mu\Omega$  cm (low resistivity), 50  $\mu\Omega$  cm (medium resistivity), and 100  $\mu\Omega$  cm (high resistivity). Similarly, analyzing the effect of

filament resistivity on different RRAM devices, we have chosen three observations for filament radius size in the order of 10 nm (low size), 50 nm (medium size), and 100 nm (high size).

#### 3. RESULT AND DISCUSSION

In the present investigation, different RRAM materials are analyzed such as ZnO, TiO2, WO3 and HfO2 owing to their fine memory performance [3-5]. The various physical parameters of above RRAM materials are listed in table 1. For the first case, the radius of the filament is varied from 10 nm to 100 nm and its effect on the saturated temperature of different RRAM material is analyzed. For each case, the resistivity of the filament is changed as 10  $\mu\Omega$  cm, 50  $\mu\Omega$  cm, and 100  $\mu\Omega$  cm. Fig 2 (a) represent the effect of filament radius on the saturated temperature of ZnO based RRAM device. The results suggest that as the radius of the filament increases the corresponding saturated temperature decreases. It is also seen that saturated temperature decreases as the resistivity of the filament increases.

Fig 2 (b to d) present the effect of filament radius on the saturated temperature of TiO2, WO3, and HfO2 based RRAM devices. The results clearly show the inverse relationship between the radius of the filament and saturated temperature for each RRAM devices. The results also prompt that WO<sub>3</sub> based RRAM device has a lower value of saturated temperature than its other counterparts for the same value of radius and resistivity of the conducting filament. The HfO2 based RRAM device shows the higher value of saturated temperature than other RRAM materials for the same value of radius and resistivity of the conducting filament. The results also indicate that there is a steady change in the saturated temperature value at medium (50  $\mu\Omega$  cm) and higher (100  $\mu\Omega$  cm) value of the filament resistivity, but sudden change is observed at the lower (10  $\mu\Omega$  cm) value of the filament resistivity. The same may be attributed to the lower value of filament resistivity invoking a high current density conductive filament that gives rise to higher heat dissipation.

For the second case, the resistivity of the filament is varied in the range of  $10~\mu\Omega$  cm to  $100~\mu\Omega$  cm with  $10~\mu\Omega$  cm as a step size and its effect on the saturated temperature of different RRAM material is analyzed. For each case, the filament radius is changed as 10~nm, 50~nm, and 100~nm. Fig 3 (a) represent the effect of filament resistivity on the saturated temperature of ZnO based RRAM. The results suggest that saturated temperature decreases with increase in filament resistivity. It is also seen that saturated temperature decreases as the radius of the filament increases from 10~nm to 100~nm.

Fig 3 (b to d) represent the effect of filament resistivity on saturated temperature for  $TiO_2$ ,  $WO_3$ , and  $HfO_2$  based RRAM devices. Here too an inverse relationship is observed between the resistivity of the filament and saturated temperature. The results indicate that  $WO_3$  based RRAM device has a lower value of saturated temperate than its other counterparts for the same value of filament resistivity and radius. The  $HfO_2$  based RRAM device shows the higher value of saturated temperature than other RRAM materials for the same value of filament resistivity and radius.

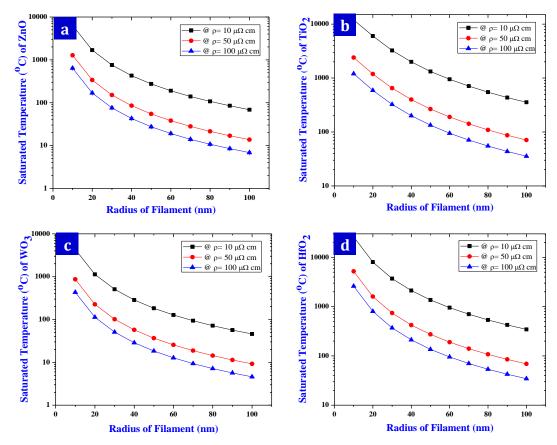


Fig. 2 – Effect of the radius of the filament on the saturated temperature of (a) ZnO; (b) TiO2; (c) WO3; and (d) HfO2 based RRAM

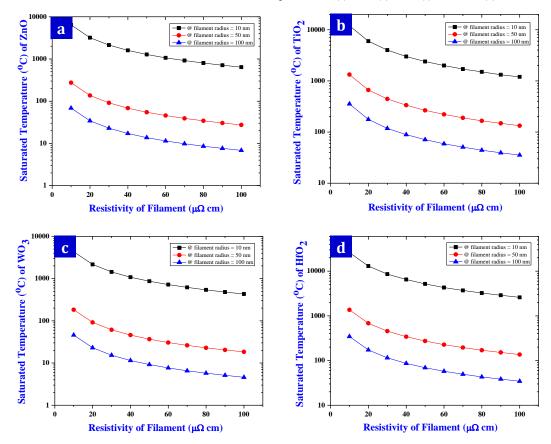


Fig. 3 – Effect of the resistivity of the filament on the saturated temperature of (a) ZnO; (b) TiO2; (c) WO3; and (d) HfO2 based RRAM.

**Table 2** – Saturated temperature change factor for the change in the filament resistivity value

RRAM	Saturated	Saturated
Material	Temperature	Temperature
	Change Factor	Change Factor
	for 10 $\mu\Omega$ cm to	for $50 \mu\Omega$ cm to
	$50~\mu\Omega~\mathrm{cm}$	$100~\mu\Omega~cm$
ZnO	5.0022	1.9997
${ m TiO_2}$	5.0050	1.9983
$WO_3$	5.0006	1.9998
$HfO_2$	5.0031	1.9994

**Table 3** – Saturated temperature change factor for the change in the filament radius value

RRAM Material	Saturated Temperature Change Factor for 10 nm to 50	Saturated Temperature Change Factor for 50 nm to 100
	nm	nm
ZnO	23.3900	3.9908
${ m TiO_2}$	8.9945	3.7538
$WO_3$	23.5772	3.9927
$HfO_2$	19.0357	3.9540

Table 2 and 3 represents the saturated temperature change factor for the change in the filament resistivity and radius of different materials. The results prompt that, the rate of change in the saturated temperature is higher for the lower resistivity whereas it becomes small for higher filament resistivity. The variation in the filament radius shows same behavior for ZnO, WO<sub>3</sub> and

 $HfO_2$  based RRAM devices however  $TiO_2$  material shows a small variation in the change factor. This is attributed to the higher thermal conductivity and specific heat capacity of  $TiO_2$  as compared to other ones taken in this investigation. The results showcase that the saturated temperature is the filament size and resistivity dependent property.

#### 4. CONCLUSION

In the present investigation, we have thoroughly investigated the thermal reaction model of RRAM device for different material and consequently correlated the relationship between saturated temperature, filament radius, and resistivity. The saturated temperature is one of the important property of high performance RRAM and it is associated with the device reliability. The higher value of the saturated temperature degrades the device reliability [22]. At the higher temperature more sensitive detection circuitry is required to read and write operations of RRAM [5, 21-22]. The results suggest that the saturated temperature decreases as the radius and resistivity of the conductive filament increases. The results also indicate that there is a sudden change in the saturated temperature for the lower value of filament radius and resistivity whereas steady change is observed for the medium and higher value of the filament radius and resistivity. Results confirm the saturated temperature is filament size and resistivity dependent property. All these investigations and results thereof are definitely significant in the application scenario of RRAMs.

### REFERENCES

- 1. A. Sawa, Mater. Today. 11, 28 (2008).
- L.M. Kukreja, A.K. Das, P. Misra, Bull. Mater. Sci. 32, 247 (2009)
- T.D. Dongale, S.S. Shinde, R.K. Kamat, K.Y. Rajpure, J. Alloy. Compd. 593, 267 (2014).
- T.D. Dongale, S.V. Mohite, A.A. Bagade, P.K. Gaikwad, P.S. Patil, R.K. Kamat, K.Y. Rajpure, *Electron. Mater. Lett.* 11, 944 (2015).
- T.D. Dongale, K.V. Khot, S.S. Mali, P.S. Patil, P.K. Gaikwad, R.K. Kamat, P.N. Bhosale, *Mater. Sci. Semicond. Process.* 40, 523 (2015).
- I. Valov, R. Waser, J.R. Jameson, M.N. Kozicki, Nanotechnology 22, 254003 (2011).
- H. Da, W. Jun-Jie, T. Yu-Hua, Chin. Phys. B 22, 038401 (2013).
- A. Redaelli, A. Pirovano, F. Pellizzer, A.L. Lacaita, D. Ielmini, R. Bez, *IEEE Electron. Device Lett.* 25, 684 (2004).
- C. Hu, M.D. McDaniel, A. Posadas, A.A. Demkov, J.G. Ekerdt, E.T. Yu, Nano Lett. 14, 4360 (2014).
- T.D. Dongale, K.P. Patil, S.B. Mullani, K.V. More, S.D. Delekar, P.S. Patil, P.K. Gaikwad, R.K. Kamat, Mater. Sci. Semicond. Process. 35, 174 (2015).
- 11. T.D. Dongale, K.P. Patil, P.K. Gaikwad, R.K. Kamat,

- Mater. Sci. Semicond. Process. 38, 228 (2015).
- 12. T.D. Dongale, K.P. Patil, S.R. Vanjare, A.R. Chavan, P.K. Gaikwad, R.K. Kamat, *J. Comput. Sci.* 11, 82 (2015).
- T.D. Dongale, P.J. Patil, K.P. Patil, S.B. Mullani, K.V. More, S.D. Delekar, P.K. Gaikwad, R.K. Kamat, J. Nano- Electron. Phys. 7, 03012 (2015).
- 14. T.D. Dongale, J. Int. Health Inf. 2, 15 (2013).
- 15. S.S. Shinde, T.D. Dongle, *J. Semicond.* **36**, 034001 (2015).
- J. Shang, G. Liu, H. Yang, X. Zhu, X. Chen, H. Tan, B. Hu,
   L. Pan, W. Xue, and R. W. Li, *Adv. Funct. Mater.* 24, 2171 (2014).
- M. Wang, C. Bi, L. Li, S. Long, Q. Liu, H. Lv, N. Lu, P. Sun, M. Liu, *Nat. Commun.* 5, 4598 (2014).
- M. Yi, Y. Cao, H. Ling, Z. Du, L. Wang, T. Yang, Q. Fan, L. Xie, W. Huang, *Nanotechnology* 25, 185202 (2014).
- R. Fang, W. Chen, L. Gao, W. Yu, S. Yu, *IEEE Electron*. Device Lett. 36, 567 (2015).
- T. Tsuruoka, K. Terabe, T. Hasegawa, M. Aono, Nanotechnology 22, 254013 (2011).
- Y. Sato, K. Kinoshita, M. Aoki, Y. Sugiyama, *Appl. Phys. Lett.* 90, 033503 (2007).
- A. Mazady, M. Anwar, *IEEE T. Electron. Dev.* 61, 1054 (2014).